

**Amendments to the Claims:**

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A circuit board device having a power supply region divided into two or more power supply regions by a slit and a ground region adjacent to the power supply region, wherein, when at least one region of said two or more power supply regions and the ground region that are adjacent has a shape that may be considered a track having a length that is larger than its width, a terminal element having an impedance that is substantially equal to a characteristic impedance between said at least one region and said ground region is connected between said power supply region and said ground region at ~~a~~ terminal portion vicinity of a terminal end of said at least one region.
2. (Withdrawn) The circuit board device according to claim 1, further comprising a dielectric layer, wherein the power supply region and the ground region that are adjacent are formed in different layers with the dielectric layer interposed therebetween and substantially oppose one another and overlap.
3. (Withdrawn) The circuit board device according to claim 1, further comprising a circuit, which has a plurality of via holes that forms a parallel circuit, formed adjacent to the terminal portion, and the terminal element is connected between the layer via the parallel circuit.
4. (Canceled)
5. (Original) The circuit board device according to claim 1, wherein the terminal element includes a capacitor.
6. (Original) The circuit board device according to claim 1, wherein the terminal element includes a resistor and a capacitor which are series-connected.
7. (Canceled)

8. (Original) The circuit board device according to claim 1, wherein an impedance  $Z_r$  of the terminal element is set such that a characteristic impedance  $Z_e$  and the impedance  $Z_r$  of the terminal element satisfy a relationship  $0.1 \leq Z_r/Z_e \leq 10$ .

9. (Original) The circuit board device according to claim 6, wherein a relationship between a characteristic impedance  $Z_e$  and a resistance  $R$  of the resistor satisfies  $(Z_e/5) \leq R \leq (5 \cdot Z_e)$ , and a capacity  $C$  of the capacitor satisfies  $C \geq 1/(10\pi \cdot f_{\min} \cdot Z_e)$ , where  $f_{\min}$  is a lower limit of a radiation noise frequency of a reduction target.

10-14. (Canceled).

15. (Currently Amended) A circuit board device having a power supply region and a ground region that are adjacent and are formed in the same ~~layer~~ plane, wherein, when at least one region of the power supply region and the ground region that are adjacent has a shape that may be considered a track, a terminal element having an impedance that is substantially equal to a characteristic impedance between said regions is connected between the power supply region and the ground region at a terminal portion vicinity of said at least one region.

16. (Previously Presented) The circuit board device according to claim 15, wherein the terminal element includes a capacitor.

17. (Previously Presented) The circuit board device according to claim 15, wherein the terminal element includes a resistor and a capacitor which are series-connected.

18. (Previously Presented) The circuit board device according to claim 15, wherein the region that may be considered a track is a shape having a length that is larger than a width.

19. (Previously Presented) The circuit board device according to claim 15, wherein an impedance  $Z_r$  of the terminal element is set such that a characteristic impedance  $Z_e$  and the impedance  $Z_r$  of the terminal element satisfy a relationship  $0.1 \leq Z_r/Z_e \leq 10$ .

20. (Previously Presented) The circuit board device according to claim 17, wherein a relationship between a characteristic impedance  $Z_e$  and a resistance  $R$  of the resistor satisfies  $(Z_e/5) \leq R \leq (5 \cdot Z_e)$ , and a capacity  $C$  of the capacitor satisfies  $C \geq 1/(10\pi \cdot f_{\min} \cdot Z_e)$ , where  $f_{\min}$  is a lower limit of a radiation noise frequency of a reduction target.

21. (New) The circuit board device according to claim 1, wherein the slit has a T-shape.

22. (New) The circuit board device according to claim 15, wherein the power supply region is entirely surrounded by the ground region.

23. (New) The circuit board device according to claim 15, wherein the power supply region and the ground region face each other without overlapping.

24. (New) The circuit board device according to claim 15, wherein the power supply region and the ground region have the same shape.

25. (New) The circuit board device according to claim 1, wherein terminal element is connected between said power supply region and said ground region only at said terminal end of said at least one region.

26. (New) The circuit board device according to claim 1, wherein an outer periphery of said power supply region and said ground region is free from connection by said terminal element.

27. (New) The circuit board device according to claim 15, wherein the terminal element is connected between said power supply region and said ground region only at said terminal end of said at least one region.

28. (New) The circuit board device according to claim 15, wherein an outer periphery of said power supply region and said ground region is free from connection by said terminal element.